

CURRENT 3.0 Ampere
VOLTAGE RANG 20 to 40 Volts

1N5820 THRU 1N5822

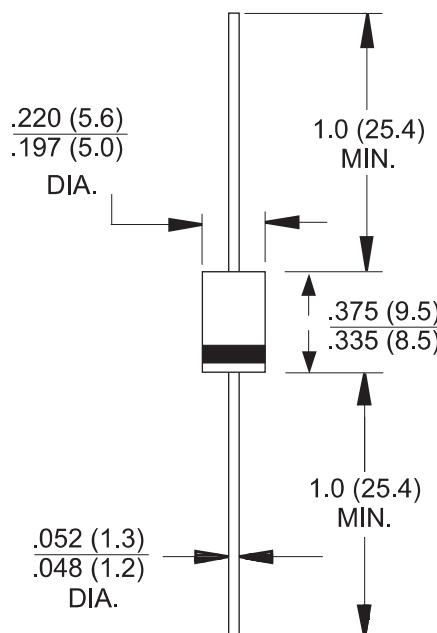
Features

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Surge Capability
- High Current Capability and Low Forward Voltage Drop
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Application
- Plastic Material: UL Flammability Classification Rating 94V-0

Mechanical Data

- Case: Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 1.1 grams (approx)
- Mounting Position: Any
- Marking: Type Number

DO-27 / DO-201AD



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

@ $T_A = 25^\circ\text{C}$ unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

| Characteristic | Symbol | 1N5820 | 1N5821 | 1N5822 | Unit |
|---|-----------------|----------------|----------------|----------------|--------------------|
| Peak Repetitive Reverse Voltage | V_{RRM} | | | | |
| Working Peak Reverse Voltage | V_{RWM} | 20 | 30 | 40 | V |
| DC Blocking Voltage | V_R | | | | |
| RMS Reverse Voltage | $V_{R(RMS)}$ | 14 | 21 | 28 | V |
| Average Rectified Output Current (Note 1) | I_O | | 3.0 | | A |
| $\text{@ } T_L = 95^\circ\text{C}$ | | | | | |
| Non-Repetitive Peak Forward Surge Current single half sine-wave superimposed on rated load (JEDEC Method) | I_{FSM} | | 80 | | A |
| Forward Voltage (Note 2) | V_{FM} | 0.475 0.850 | 0.500 0.900 | 0.525 0.950 | V |
| Peak Reverse Current at Rated DC Blocking Voltage (Note 2) | I_{RM} | | 2.0 20 | | mA |
| $\text{@ } T_A = 25^\circ\text{C}$ $\text{@ } T_A = 100^\circ\text{C}$ | | | | | |
| Typical Thermal Resistance (Note 3) | $R_{\theta JA}$ | | 40 | | $^\circ\text{C/W}$ |
| | $R_{\theta JL}$ | | 10 | | |
| Operating and Storage Temperature Range | T_j, T_{STG} | | -65 to +125 | | $^\circ\text{C}$ |

Notes:

1. Measured at ambient temperature at a distance of 9.5mm from the case.
2. Short duration pulse test used to minimize self-heating effect.
3. Thermal resistance from junction to lead vertical P.C.B. mounted, 0.500" (12.7mm) lead length with 2.5 x 2.5" (63.5 x 63.5mm) copper pad.

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RATING AND CHARACTERISTIC CURVES 1N5820 Thru 1N5822

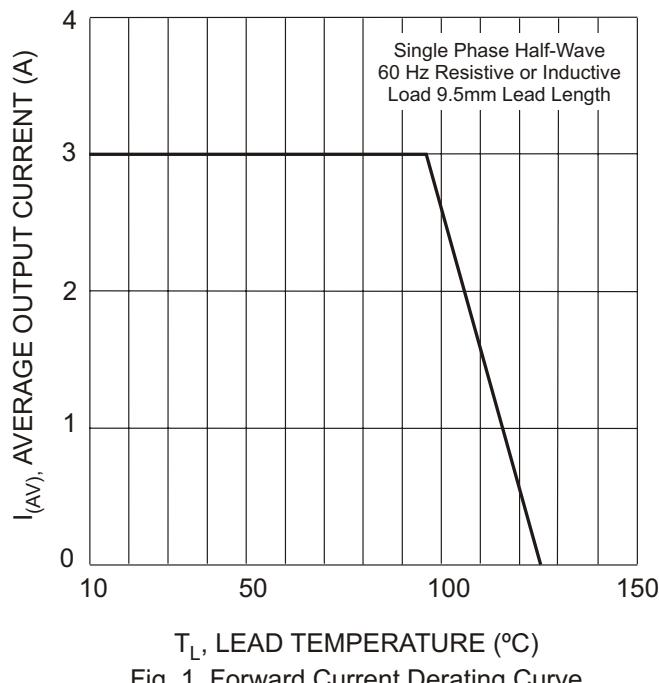


Fig. 1 Forward Current Derating Curve

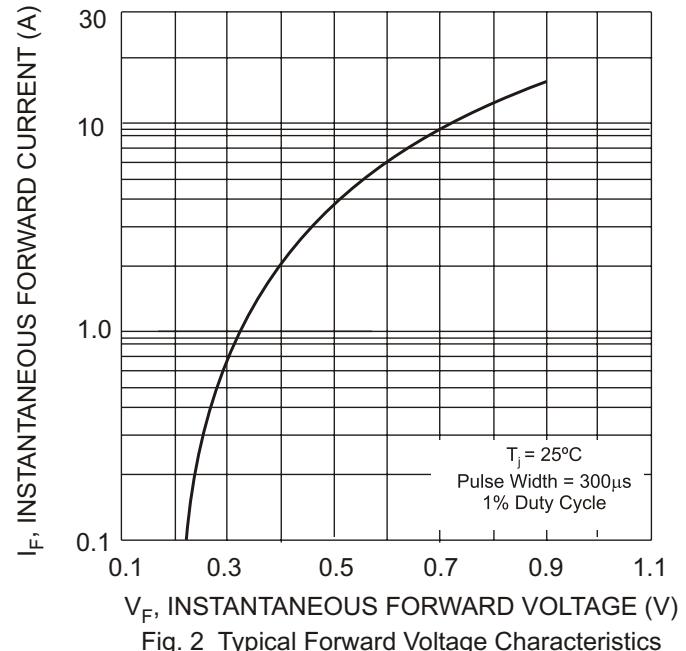


Fig. 2 Typical Forward Voltage Characteristics

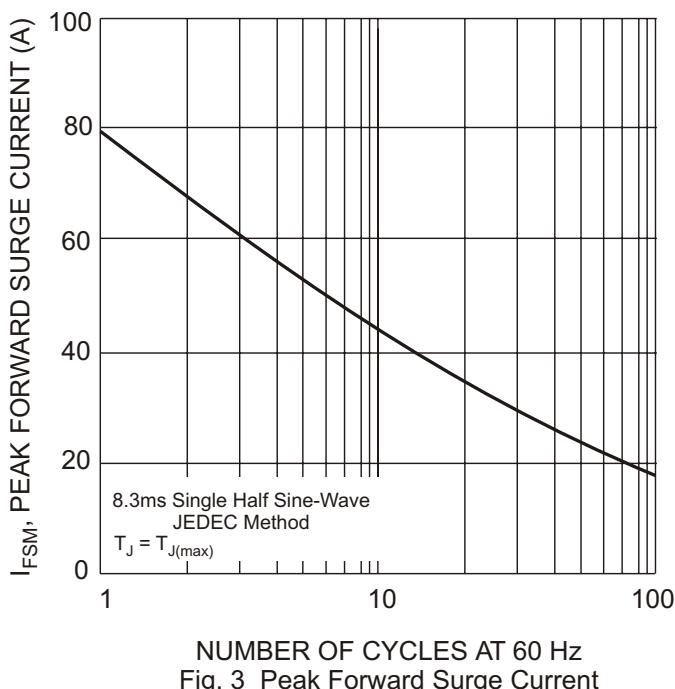


Fig. 3 Peak Forward Surge Current

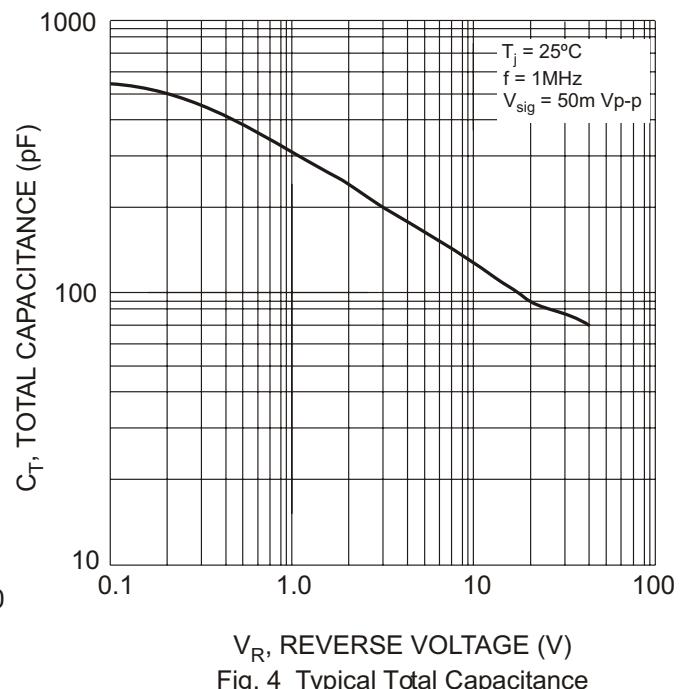


Fig. 4 Typical Total Capacitance